

Electronic Patent Application Fee Transmittal

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|--|--|-----------------|---------------|-----------------------------|
| Application Number: | 10779904 | | | |
| Filing Date: | 17-Feb-2004 | | | |
| Title of Invention: | Method and apparatus for defect analysis of semiconductor integrated circuit | | | |
| First Named Inventor/Applicant Name: | Masahiro Ishida | | | |
| Filer: | Thomas K. Scherer/Sophie Bolt | | | |
| Attorney Docket Number: | 02008.071003 | | | |
| Filed as Large Entity | | | | |
| Utility under 35 USC 111(a) Filing Fees | | | | |
| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) |
| Basic Filing: | | | | |
| Pages: | | | | |
| Claims: | | | | |
| Miscellaneous-Filing: | | | | |
| Petition: | | | | |
| Patent-Appeals-and-Interference: | | | | |
| Post-Allowance-and-Post-Issuance: | | | | |
| Extension-of-Time: | | | | |

| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) |
|-----------------------------------|----------|----------|--------|----------------------|
| Miscellaneous: | | | | |
| Request for continued examination | 1801 | 1 | 810 | 810 |
| Total in USD (\$) | | | | 810 |